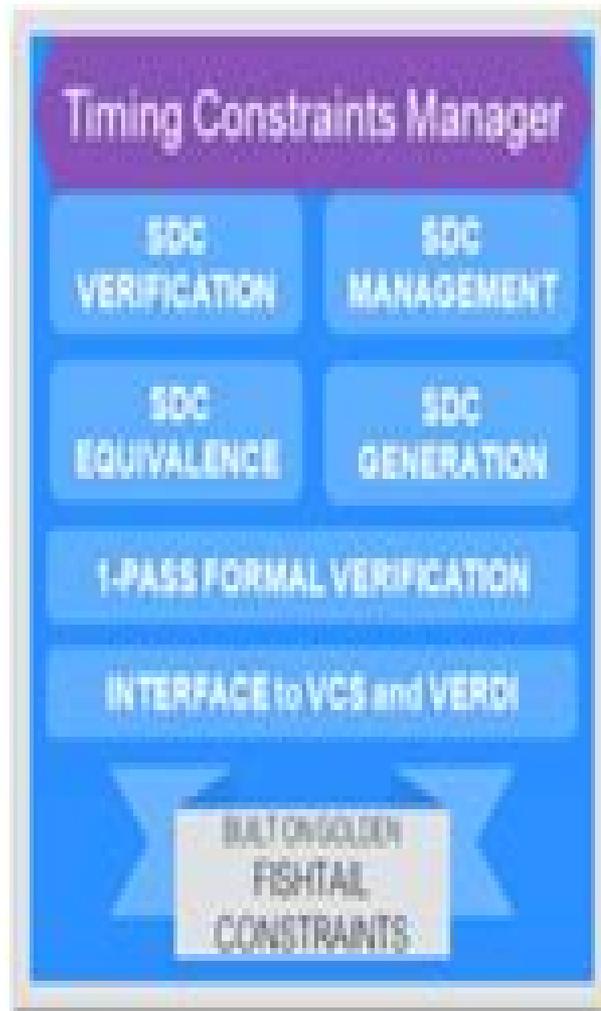


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Built on FishTail's Best-in-Class SDC Constraints Solution



- Comprehensive SDC Timing Constraints Generation, Verification and Management
- SDC Verification with No Noise
 - SDC Signoff at RTL handoff using proprietary formal & assertion technology
- Automated, Accurate SDC Promotion
 - Saves weeks and eliminates bugs from manual reuse of IP constraints in SoCs
- Automated SDC Generation from RTL
 - Improves designer productivity and design PPA

90% Reduction
in Designer SDC
Review Effort

Auto SDC Gen/Mgmt
Shortens SoC Execution
by 6-8 Weeks

Eliminate Silicon
Failure from SDC Bugs

Synopsys Timing Constraints And Optimization

AN Whitehead



Synopsys Timing Constraints And Optimization :

Synopsys Timing Constraints And Optimization Book Review: Unveiling the Magic of Language

In a digital era where connections and knowledge reign supreme, the enchanting power of language has been apparent than ever. Its power to stir emotions, provoke thought, and instigate transformation is really remarkable. This extraordinary book, aptly titled "**Synopsys Timing Constraints And Optimization**," written by a very acclaimed author, immerses readers in a captivating exploration of the significance of language and its profound effect on our existence. Throughout this critique, we shall delve into the book's central themes, evaluate its unique writing style, and assess its overall influence on its readership.

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