

Embedded Systems

ARM Programming and Optimization

Jason D. Bakos

```
#pragma omp parallel for
for (i=0;i<N/4;i+=4) {
    asm("pld %[nextx]\n\t"
        "mov r8, [coefs_addr]\n\t"
        "vld1.32 {q0}, [%x_addr]#128\n\t"
        "vld1.32 {q1}, [%x]#64\n\t"
        "vld1.32 {q2}, [%x]#128\n\t"
        "vld1.32 {q3}, [%x]#128\n\t"
        "vld1.32 {q4}, [%x]#128\n\t"
        "vld1.32 {q5}, [%x]#128\n\t"
        "vld1.32 {q6}, [%x]#128\n\t"
        "vld1.32 {q7}, [%x]#128\n\t"
        "vld1.32 {q8}, [%x]#128\n\t"
        "vmla.f32 q2, q0, q1\n\t"
        "vmla.f32 q3, q0, q2\n\t"
        "vmla.f32 q4, q0, q3\n\t"
        "vmla.f32 q5, q0, q4\n\t"
        "vmla.f32 q6, q0, q5\n\t"
        "vmla.f32 q7, q0, q6\n\t"
        "vmla.f32 q8, q0, q7\n\t"
        "vst1.32 {q0}, [%d_addr]#128\n\t"
        "w(x[i+16]) : : [%x_addr]#4*(x[i]), [nextx-]
        ]#4*(x[i+16]), [%d_addr]#4*(d[i]), [coefs_addr]#4*(coefs_4vector) : : r0, r1, r2, r3, q1,
        q2, q3");
}
```

MK
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