GPU Accelerator Capabilities *
Release 18.2

Support by Applications

ANSYS Mechanical APDL supports NVIDIA CUDA-enabled Tesla and Quadro series workstation and server cards. When using the same action or eigensolvers based on the same action with NVIDIA cards additional considerations apply (please consult the ANSYS installation guide for details).

ANSYS Fluent supports NVIDIA CUDA-enabled Tesla and Quadro series workstation and server cards.

ANSYS Polyflow supports NVIDIA CUDA-enabled Tesla and Quadro series workstation and server cards.

ANSYS EMIT supports NVIDIA Tesla and Quadro G series, P series, M series and K series cards, GeForce GTX Series and GeForce GT Series.

ANSYS HFSS supports NVIDIA Tesla K and P series, C20 series, Tesla K series, Quadro K, P and K series cards and above.

ANSYS ICEPAK supports NVIDIA CUDA-enabled Tesla and Quadro series workstation and server cards.

ANSYS Maxwell supports NVIDIA Tesla K and P series, C20 series, Tesla K series, Quadro K, P and K series cards and above.

ANSYS Savant supports NVIDIA Tesla and Quadro G series, P series, M series and K series cards, GeForce GTX Series and GeForce GT Series.

Application	Manufacturer	Product Series	Card / GPU	Tested Platform	Tested Operating System Version
ANSYS Mechanical APDL	NVIDIA	Tesla	K80	Linux x64	Red Hat 6.6
			M2075	Linux x64	Red Hat 7.0
			P100	Windows x64	Windows 10
				Linux x64	CentOS 7.0
			V100	Windows x64	Windows Server (2016)
				Linux x64	CentOS 7.0
ANSYS Fluent	NVIDIA	Quadro K	K P100	Linux x64	Red Hat 7.0
			K5000	Windows x64	Windows 7
		Tesla	C2075	Windows x64	Windows 7
			K40	Windows x64	Windows 10
			K80	Linux x64	Red Hat 7.0
					SLES (11 SP1)
			M2075	Windows x64	Windows 7
			P100	Linux x64	SLES (11 SP1)

Application	Manufacturer	Product Series	Card / GPU	Tested Platform	Tested Operating System Version
ANSYS Polyflow	NVIDIA	Quadro K	K4000	Linux x64	Red Hat 7.0
			K5000	Windows x64	Windows 7
			M4000	Windows x64	Windows 10
					Windows Server (2016)
				Linux x64	SLES (11 SP1)
					CentOS 7.0
			M4000	Linux x64	Red Hat 6.6
					SLES (11 SP1)
					CentOS 7.0
			P4000 (Dual)	Windows x64	Windows 10
		Tesla	K20x	Windows x64	Windows 7
			K80	Linux x64	Red Hat 7.0
ANSYS EMIT	NVIDIA	Quadro K	K V100	Windows x64	Windows 10
			M4000	Windows x64	Windows 10
				Linux x64	SLES (11 SP1)
		Tesla	K20x	Windows x64	Windows 10
			K40	Windows x64	Windows 7
			P100	Windows x64	Windows Server (2016)
ANSYS HFSS	NVIDIA	Quadro K	K P100	Windows x64	Windows 10
			K V100	Windows x64	Windows 10
		Tesla	K80	Windows x64	Windows 10
			K80	Linux x64	CentOS 7.0
			P100	Windows x64	Windows Server (2016)
					Windows Server (2016)
				Linux x64	SLES (11 SP1)
			V100	Linux x64	SLES (11 SP1)

Gpu Accelerator And Co Processor Capabilities Ansys

Yi Zou

Gpu Accelerator And Co Processor Capabilities Ansys:

Implementation and Performance Analysis of Many-body Quantum Chemical Methods on the Intel Xeon Phi Coprocessor and NVIDIA GPU Accelerator Bobo Shi,2016 The tensor contraction are performed using BLAS DGEMM on coprocessor accelerator Then the result is post processed using a 6 dimensional loop For Intel Xeon Phi implementation OpenMP is used to bind threads to physical processing units on Xeon Phi coprocessors The OpenMP threads affinity are tuned for Intel Xeon Phi Coprocessor to obtain best performance For GPU a algorithm is designed to map the 6 dimensional loop post processing to CUDA threads gridDim and blockDim are tuned to reach best performance 4x and 9x 13x overall speedup is obtained for Intel Xeon Phi and GPU implementation respectively   **Modern Accelerator Technologies for Geographic Information Science** Xuan Shi,Volodymyr Kindratenko,Chaowei Yang,2013-10-27 This book explores the impact of augmenting novel architectural designs with hardware based application accelerators The text covers comprehensive aspects of the applications in Geographic Information Science remote sensing and deploying Modern Accelerator Technologies MAT for geospatial simulations and spatiotemporal analytics MAT in GIS applications MAT in remotely sensed data processing and analysis heterogeneous processors many core and highly multi threaded processors and general purpose processors are also presented This book includes case studies and closes with a chapter on future trends Modern Accelerator Technologies for GIS is a reference book for practitioners and researchers working in geographical information systems and related fields Advanced level students in geography computational science computer science and engineering will also find this book useful

Coprocessor Acceleration for Domain-specific Computing Yi Zou,2012 There is a growing trend to use coprocessors to offload and accelerate domain specific applications in order to obtain significant performance improvement and energy power reductions Two important coprocessor components in the heterogeneous system are the GPU and FPGA GPU graphics processing unit is increasingly used as a data parallel coprocessor for general computations The newest GPU has a much larger number of cores compared to CPU and very high peak FLOPS FPGA field programmable gate array on the other hand allows users to customize at fine grain level the computational data path and memory hierarchy according to the exact need of the applications FPGA excels in integer operations and bit level operations The thesis starts with several coprocessor acceleration examples for our focus application domains the first domain is on VLSICAD algorithms and the second is on computational medical imaging We detail application acceleration examples in the domains including lithography simulation for IC manufacturing medical image reconstruction using compressive sensing and medical image registration using fluid models Both GPU accelerated versions and FPGA accelerated versions have been implemented Based on these implementations we then analyze the performance and energy trade offs the interaction between the diverse application requirements and a spectrum of hardware systems and how those domain specific coprocessor acceleration case studies further bring us insights for domain specific architecture innovations In the end we showcase an example for collaborative

execution on the heterogeneous platform Different scheduling policies are needed to optimize performance or energy The thesis concludes as we present reusable architecture templates and realizations for futuristic accelerator rich CMPs

The book delves into Gpu Accelerator And Co Processor Capabilities Ansys. Gpu Accelerator And Co Processor Capabilities Ansys is a vital topic that needs to be grasped by everyone, from students and scholars to the general public. The book will furnish comprehensive and in-depth insights into Gpu Accelerator And Co Processor Capabilities Ansys, encompassing both the fundamentals and more intricate discussions.

1. The book is structured into several chapters, namely:

   - Chapter 1: Introduction to Gpu Accelerator And Co Processor Capabilities Ansys
   - Chapter 2: Essential Elements of Gpu Accelerator And Co Processor Capabilities Ansys
   - Chapter 3: Gpu Accelerator And Co Processor Capabilities Ansys in Everyday Life
   - Chapter 4: Gpu Accelerator And Co Processor Capabilities Ansys in Specific Contexts
   - Chapter 5: Conclusion

2. In chapter 1, the author will provide an overview of Gpu Accelerator And Co Processor Capabilities Ansys. This chapter will explore what Gpu Accelerator And Co Processor Capabilities Ansys is, why Gpu Accelerator And Co Processor Capabilities Ansys is vital, and how to effectively learn about Gpu Accelerator And Co Processor Capabilities Ansys.
3. In chapter 2, this book will delve into the foundational concepts of Gpu Accelerator And Co Processor Capabilities Ansys. This chapter will elucidate the essential principles that must be understood to grasp Gpu Accelerator And Co Processor Capabilities Ansys in its entirety.
4. In chapter 3, the author will examine the practical applications of Gpu Accelerator And Co Processor Capabilities Ansys in daily life. This chapter will showcase real-world examples of how Gpu Accelerator And Co Processor Capabilities Ansys can be effectively utilized in everyday scenarios.
5. In chapter 4, this book will scrutinize the relevance of Gpu Accelerator And Co Processor Capabilities Ansys in specific contexts. This chapter will explore how Gpu Accelerator And Co Processor Capabilities Ansys is applied in specialized fields, such as education, business, and technology.
6. In chapter 5, the author will draw a conclusion about Gpu Accelerator And Co Processor Capabilities Ansys. This chapter will summarize the key points that have been discussed throughout the book.
   This book is crafted in an easy-to-understand language and is complemented by engaging illustrations. It is highly recommended for anyone seeking to gain a comprehensive understanding of Gpu Accelerator And Co Processor Capabilities Ansys.

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Gpu Accelerator And Co Processor Capabilities Ansys Introduction

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