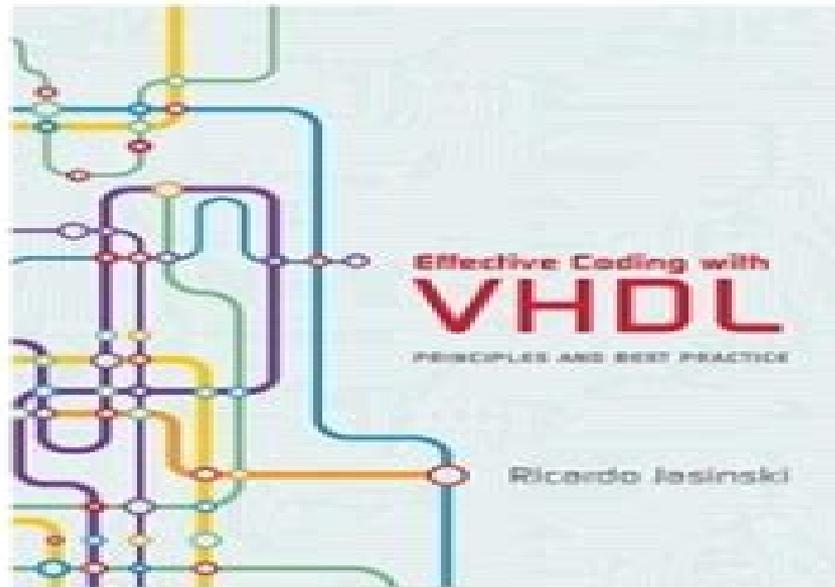


# Effective Coding with VHDL: Principles and Best Practice (The MIT Press)



# Effective Coding With Vhdl Principles And Best Practice

**Chang**



## **Effective Coding With Vhdl Principles And Best Practice:**

*Effective Coding with VHDL* Ricardo Jasinski, 2016-05-27 A guide to applying software design principles and coding practices to VHDL to improve the readability maintainability and quality of VHDL code This book addresses an often neglected aspect of the creation of VHDL designs A VHDL description is also source code and VHDL designers can use the best practices of software development to write high quality code and to organize it in a design This book presents this unique set of skills teaching VHDL designers of all experience levels how to apply the best design principles and coding practices from the software world to the world of hardware The concepts introduced here will help readers write code that is easier to understand and more likely to be correct with improved readability maintainability and overall quality After a brief review of VHDL the book presents fundamental design principles for writing code discussing such topics as design quality architecture modularity abstraction and hierarchy Building on these concepts the book then introduces and provides recommendations for each basic element of VHDL code including statements design units types data objects and subprograms The book covers naming data objects and functions commenting the source code and visually presenting the code on the screen All recommendations are supported by detailed rationales Finally the book explores two uses of VHDL synthesis and testbenches It examines the key characteristics of code intended for synthesis distinguishing it from code meant for simulation and then demonstrates the design and implementation of testbenches with a series of examples that verify different kinds of models including combinational sequential and FSM code Examples from the book are also available on a companion website enabling the reader to experiment with the complete source code

### **Effective Coding with VHDL**

Ricardo Jasinski, 2016-05-27 A guide to applying software design principles and coding practices to VHDL to improve the readability maintainability and quality of VHDL code This book addresses an often neglected aspect of the creation of VHDL designs A VHDL description is also source code and VHDL designers can use the best practices of software development to write high quality code and to organize it in a design This book presents this unique set of skills teaching VHDL designers of all experience levels how to apply the best design principles and coding practices from the software world to the world of hardware The concepts introduced here will help readers write code that is easier to understand and more likely to be correct with improved readability maintainability and overall quality After a brief review of VHDL the book presents fundamental design principles for writing code discussing such topics as design quality architecture modularity abstraction and hierarchy Building on these concepts the book then introduces and provides recommendations for each basic element of VHDL code including statements design units types data objects and subprograms The book covers naming data objects and functions commenting the source code and visually presenting the code on the screen All recommendations are supported by detailed rationales Finally the book explores two uses of VHDL synthesis and testbenches It examines the key characteristics of code intended for synthesis distinguishing it from code meant for simulation and then demonstrates the design and

implementation of testbenches with a series of examples that verify different kinds of models including combinational sequential and FSM code Examples from the book are also available on a companion website enabling the reader to experiment with the complete source code

*Digital System Design with VHDL* Mark Zwoliński, 2000 Electronic systems based on digital principles are becoming ubiquitous A good design approach to these systems is essential and a top down methodology is favoured Such an approach is vastly simplified by the use of computer modeling to describe the systems VHDL is a formal language which allows a designer to model the behaviours and structure of a digital circuit on a computer before implementation Digital System Design with VHDL is intended both for students on Digital Design courses and practitioners who would like to integrate digital design and VHDL synthesis in the workplace Its unique approach combines the principles of digital design with a guide to the use of VHDL Synthesis issues are discussed and practical guidelines are provided for improving simulation accuracy and performance Features a practical perspective is obtained by the inclusion of real life examples an emphasis on software engineering practices encourages clear coding and adequate documentation of the process demonstrates the effects of particular coding styles on synthesis and simulation efficiency covers the major VHDL standards includes an appendix with examples in Verilog

**Index to IEEE Publications** Institute of Electrical and Electronics Engineers, 1996

**The British National Bibliography** Arthur James Wells, 2000

**VHDL Answers to Frequently Asked Questions** Ben Cohen, 1998-01-31 VHDL Answers to Frequently asked Questions is a follow up to the author's book VHDL Coding Styles and Methodologies ISBN 0 7923 9598 0 On completion of his first book the author continued teaching VHDL and actively participated in the comp lang vhdl newsgroup During his experiences he was enlightened by the many interesting issues and questions relating to VHDL and synthesis These pertained to misinterpretations in the use of the language methods for writing error free and simulation efficient code for testbench designs and for synthesis and general principles and guidelines for design verification As a result of this wealth of public knowledge contributed by a large VHDL community the author decided to act as a facilitator of this information by collecting different classes of VHDL issues and by elaborating on these topics through complete simulatable examples This book is intended for those who are seeking an enhanced proficiency in VHDL Its target audience includes 1 Engineers The book addresses a set of problems commonly experienced by real users of VHDL It provides practical explanations to the questions and suggests practical solutions to the raised issues It also includes packages of common utilities that are useful in the generation of debug code and testbench designs These packages include conversions to strings the IMAGE package generation of Linear Feedback Shift Registers LFSR Multiple Input Shift Register MISR and random number generators

*RTL Hardware Design Using VHDL* Pong P. Chu, 2006-04-20 The skills and guidance needed to master RTL hardware design This book teaches readers how to systematically design efficient portable and scalable Register Transfer Level RTL digital circuits using the VHDL hardware description language and synthesis software Focusing on the module level design

which is composed of functional units routing circuit and storage the book illustrates the relationship between the VHDL constructs and the underlying hardware components and shows how to develop codes that faithfully reflect the module level design and can be synthesized into efficient gate level implementation Several unique features distinguish the book Coding style that shows a clear relationship between VHDL constructs and hardware components Conceptual diagrams that illustrate the realization of VHDL codes Emphasis on the code reuse Practical examples that demonstrate and reinforce design concepts procedures and techniques Two chapters on realizing sequential algorithms in hardware Two chapters on scalable and parameterized designs and coding One chapter covering the synchronization and interface between multiple clock domains Although the focus of the book is RTL synthesis it also examines the synthesis task from the perspective of the overall development process Readers learn good design practices and guidelines to ensure that an RTL design can accommodate future simulation verification and testing needs and can be easily incorporated into a larger system or reused Discussion is independent of technology and can be applied to both ASIC and FPGA devices With a balanced presentation of fundamentals and practical examples this is an excellent textbook for upper level undergraduate or graduate courses in advanced digital logic Engineers who need to make effective use of today s synthesis software and FPGA devices should also refer to this book

**VHDL Coding Styles and Methodologies** Ben Cohen,2013-10-03 VHDL Coding Styles and Methodologies was originally written as a teaching tool for a VHDL training course The author began writing the book because he could not find a practical and easy to read book that gave in depth coverage of both the language and coding methodologies This book is intended for 1 College students It is organized in 13 chapters each covering a separate aspect of the language with complete examples All VHDL code described in the book is on a companion 3 5 PC disk Students can compile and simulate the examples to get a greater understanding of the language Each chapter includes a series of exercises to reinforce the concepts 2 Engineers It is written by an aerospace engineer who has 26 years of hardware software computer architecture and simulation experience It covers practical applications ofVHDL with coding styles and methodologies that represent what is current in the industry VHDL synthesizable constructs are identified Guidelines for testbench designs are provided Also included is a project for the design of a synthesizable Universal Asynchronous Receiver Transmitter UART and a testbench to verify proper operation of the UART in a realistic environment with CPU interfaces and transmission line jitter An introduction to VHDL Initiative Toward ASIC Libraries VITAL is also provided The book emphasizes VHDL 1987 standard but provides guidelines for features implemented in VHDL 1993

*VHDL* Weng Fook Lee,2000

**Digital Design and Modeling with VHDL and Synthesis** K. C. Chang,1997-10-18 Digital Systems Design with VHDL and Synthesis presents an integrated approach to digital design principles processes and implementations to help the reader design much more complex systems within a shorter design cycle This is accomplished by introducing digital design concepts VHDL coding VHDL simulation synthesis commands and strategies together The author focuses on the

ultimate product of the design cycle the implementation of a digital design VHDL coding synthesis methodologies and verification techniques are presented as tools to support the final design implementation Readers will understand how to apply and adapt techniques for VHDL coding verification and synthesis to various situations Digital Systems Design with VHDL and Synthesis is a result of K C Chang s practical experience in both design and as an instructor Many of the design techniques and considerations illustrated throughout the chapters are examples of viable designs His teaching experience leads to a step by step presentation that addresses common mistakes and hard to understand concepts in a way that eases learning Unique features of the book include the following VHDL code explained line by line to capture the logic behind the design concepts VHDL is verified using VHDL test benches and simulation tools Simulation waveforms are shown and explained to verify design correctness VHDL code is synthesized and commands and strategies are discussed Synthesized schematics and results are analyzed for area and timing Variations on the design techniques and common mistakes are addressed Demonstrated standard cell gate array and FPGA three design processes Each with a complete design case study Test bench post layout verification and test vector generation processes Practical design concepts and examples are presented with VHDL code simulation waveforms and synthesized schematics so that readers can better understand their correspondence and relationships

**A Tutorial Introduction to VHDL Programming** Orhan Gazi,2018-08-18 This book helps readers create good VHDL descriptions and simulate VHDL designs It teaches VHDL using selected sample problems which are solved step by step and with precise explanations so that readers get a clear idea of what a good VHDL code should look like The book is divided into eight chapters covering aspects ranging from the very basics of VHDL syntax and the module concept to VHDL logic circuit implementations In the first chapter the entity and architecture parts of a VHDL program are explained in detail The second chapter explains the implementations of combinational logic circuits in VHDL language while the following chapters offer information on the simulation of VHDL programs and demonstrate how to define data types other than the standard ones available in VHDL libraries In turn the fifth chapter explains the implementation of clocked sequential logic circuits and the sixth shows the implementation of registers and counter packages The book s last two chapters detail how components functions and procedures as well as floating point numbers are implemented in VHDL The book offers extensive exercises at the end of each chapter inviting readers to learn VHDL by doing it and writing good code

**Digital Electronics and Design with VHDL** Volnei A. Pedroni,2008-01-25 Digital Electronics and Design with VHDL offers a friendly presentation of the fundamental principles and practices of modern digital design Unlike any other book in this field transistor level implementations are also included which allow the readers to gain a solid understanding of a circuit s real potential and limitations and to develop a realistic perspective on the practical design of actual integrated circuits Coverage includes the largest selection available of digital circuits in all categories combinational sequential logical or arithmetic and detailed digital design techniques with a thorough discussion on state machine modeling for the analysis

and design of complex sequential systems Key technologies used in modern circuits are also described including Bipolar MOS ROM RAM and CPLD FPGA chips as well as codes and techniques used in data storage and transmission Designs are illustrated by means of complete realistic applications using VHDL where the complete code comments and simulation results are included This text is ideal for courses in Digital Design Digital Logic Digital Electronics VLSI and VHDL and industry practitioners in digital electronics Comprehensive coverage of fundamental digital concepts and principles as well as complete realistic industry standard designs Many circuits shown with internal details at the transistor level as in real integrated circuits Actual technologies used in state of the art digital circuits presented in conjunction with fundamental concepts and principles Six chapters dedicated to VHDL based techniques with all VHDL based designs synthesized onto CPLD FPGA chips *VHDL Coding Style Guidelines and Synthesis* Shahabuddin L. Inamdar,2004 The analysis was performed by designing and implementing a screensaver circuit on an FPGA and displaying it on the VGA Monitor

**Digital Systems Design With Vhdl And Synthesis: An Integrated Approach** Chang,2007-01-10 This book presents an integrated approach to digital design principles processes and implementations to help the reader design increasingly complex systems within shorter design cycles It also introduces digital design concepts VHDL coding VHDL simulation synthesis commands and strategies together VHDL and Digital Circuit Primitives VHDL Simulation and Synthesis Environment and Design Process Basic Combinational Circuits Basic Binary Arithmetic Circuits Basic Sequential Circuits Registers Clock and Reset Circuits Dual Port RAM FIFO and DRAM Modeling A Design Case Study Finite Impulse Response Filter ASIC Design A Design Case Study A Microprogram Controller Design Error Detection and Correction Fixed Point Multiplication Fixed Point Division Floating Point Arithmetic **Fundamentals Of HDL Design** Cyril Pr,2009-01-01 This book intends to meet the requirements of basic course in HDL modeling this discussions presented in this book targets audience doing their first level course in HDL design Emphasis on use of multiple examples to explain the concepts of HDL coding in Verilog and VHDL is the main advantage of this book Readers with no prior knowledge of HDL language with basic knowledge in digital design and programming skill may appreciate the contents and the concepts discussed in this book The topics presented and explained using examples are taken from most commonly used digital designs taught in every course curriculum on digital design Most of the examples are based on sub blocks required to model complex digital designs that are adopted by most of the practicing engineers Worked out examples at the end of every chapter exercises problems and discussions presented in the appendix would be more useful to a reader to learn remember and appreciate the advantages of HDLs for design and modeling Solution manual for the exercise problems can be downloaded from publisher s web link Features Includes digital design examples with HDL coding in Verilog and VHDL Provides a clear understanding between various language constructs that can be appropriately used for modeling digital systems using Verilog and VHDL Includes simple to complex examples that introduces the best industry practices for optimized HDL coding for design and verification

Concepts and examples covered meets more than the requirements for a first level course on HDL Includes more number of examples exercises and solutions in every chapter to give a better understanding of HDL Advanced concepts are discussed with examples and case studies for better understanding of the subject Synthesis Contents Introduction VHDL Data Flow Descriptions Sequential Modeling Structural Description Functions Procedures Test Benches and Mixed Signal Descriptions Appendix A Appendix B Bibliography Index

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