

System Clock (MHz) 12.0 Update Freq. 1

SR0F

R/D	M/D	TR0	TL0	R7	0x00	0	0x00
0x00	0x00	0x00	0x00	R4	0x00	ACC	0x1B
R0D	T0D	TR0D	TL0D	R5	0x00	PRM	0x01
0	0	0x00	0x00	R4	0x00	IP	0x00
SC0B	0x00	TC0B	0x00	R3	0x00	IE	0x00
				R2	0x05	PC0B	0x00
pin0	bit0	TR1	TL1	R1	0x00	DPB	0x00
0x0F	0x0F P3	0x00	0x00	R0	0x3E	DPL	0x00
0x0F	0x0F P2					SP	0x07
0x0F	0x0F P1	PC					
0x0F	0x0F P0	0x0004					

8051

Modify RAM

Data Memory

addr	0x00	0x05	value
0	3E	05	00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
10	00	00	00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
20	00	00	00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
30	00	00	00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
40	00	00	00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
50	00	00	00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
60	00	00	00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
70	00	00	00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00

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File: test51.asm - Instructions: 760

```

org 0
start:
MOV A, #0H
ADD A, #2H

loop: SJMP loop
end

```

P0.7	1	Display-select Enable CB/DAC #B
P0.6	1	Keypad Column 2
P0.5	1	Keypad Column 1
P0.4	1	Keypad Column 0
P0.3	1	Keypad Row 3
P0.2	1	Keypad Row 2
P0.1	1	Keypad Row 1
P0.0	1	Keypad Row 0
P1.7	1	LED 7 (Reg. 0x10AC) DR1/LCD DR7
P1.6	1	LED 6 (Reg. 0x10AC) DR1/LCD DR6
P1.5	1	LED 5 (Reg. 0x10AC) DR1/LCD DR5
P1.4	1	LED 4 (Reg. 0x10AC) DR1/LCD DR4
P1.3	1	LED 3 (... 0x10AC) DR1/LCD DR3
P1.2	1	LED 2 (... 0x10AC) DR1/LCD DR2
P1.1	1	LED 1 (Reg. 0x10AC) DR1/LCD DR1
P1.0	1	LED 0 (Reg. 0x10AC) DR1/LCD DR0
P1.7	0	0x 10AC DR7
P1.6	0	0x 10AC DR6
P1.5	0	0x 10AC DR5
P1.4	0	0x 10AC DR4
P1.3	0	0x 10AC DR3
P1.2	0	0x 10AC DR2
P1.1	0	0x 10AC DR1
P1.0	0	0x 10AC DR0
P1.7	0	ADC 00/Comparator Output
P1.6	0	ADC #B
P1.5	0	Motor Enable
P1.4	0	Display-select Input 1
P1.3	0	LED Gate Output/Displayrow -1 0
P1.2	0	ADC 01FB
P1.1	0	Motor Control Bit 1/Bit 1/0x1 0x07 Rx
P1.0	0	Motor Control Bit 0/Bit 0/0x1 0x07 Tx

DR LD

7 6 5 4 3 2 1 0

1 2 3 AND Gate Enabled

4 5 6 Key Bounce Disabled

7 8 9 Radio

- 0 #

U No Parity 8-bit UART @ 19200 Baud

Rx Rx Reset

Tx Tx Send

0.0V input

11111111

ADC

MAX

MIN

Motor Enabled

Scope DAC

0F 0 AC 0x00 0R 0x00 0R 0x00

8888

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S Ben Porath



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