

EMC and System-ESD Design Guidelines for Board Layout

About this document

Scope and purpose

This document provides information for EMC optimized PCB design and system ESD design. The topics covered include PCB Design considerations regarding the routing of high speed signals, selecting stack-up of the PCB, selecting decoupling components, impedance controlled design of the traces, and termination of high speed signal paths. Special considerations for microcontrollers are also provided.

Attention: This application note contains design recommendations from Infineon Technologies point of view. Effectiveness and performance of the final application implementation must be validated by the customer, based on their specific implementation choices.

Intended audience

This document is intended for anyone who needs to design EMC optimized application boards using micro-controllers and other components from Infineon Technologies AG.

Table of Contents

About this document	1
Table of Contents	1
1 Overview	3
1.1 Noise Sources	4
1.2 Coupling paths	6
1.2.1 Common-mode and differential-mode	6
2 PCB considerations	9
3 Design measures	12
3.1 Power Supply	13
3.1.1 Layout Structures	14
3.1.1.1 Two-layer boards	18
3.1.1.2 Multilayer boards	19
3.1.2 Components	22
3.1.2.1 Capacitors	22
3.1.2.2 Inductors and Ferrite Beads	29
3.2 Signals	32
3.2.1 Layout structures for two-layer and multi-layer boards	33
3.2.2 Components	41
3.2.2.1 Resistors	41
3.2.2.2 EMI Filters	42
4 System-Level ESD	43
4.1 General	43

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Table of Contents Emc And System Esd Design Guidelines For Board Layout

1. Understanding the eBook Emc And System Esd Design Guidelines For Board Layout
 - The Rise of Digital Reading Emc And System Esd Design Guidelines For Board Layout
 - Advantages of eBooks Over Traditional Books
2. Identifying Emc And System Esd Design Guidelines For Board Layout
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Emc And System Esd Design Guidelines For Board Layout
 - User-Friendly Interface
4. Exploring eBook Recommendations from Emc And System Esd Design Guidelines For Board Layout

- Personalized Recommendations
 - Emc And System Esd Design Guidelines For Board Layout User Reviews and Ratings
 - Emc And System Esd Design Guidelines For Board Layout and Bestseller Lists
5. Accessing Emc And System Esd Design Guidelines For Board Layout Free and Paid eBooks
 - Emc And System Esd Design Guidelines For Board Layout Public Domain eBooks
 - Emc And System Esd Design Guidelines For Board Layout eBook Subscription Services
 - Emc And System Esd Design Guidelines For Board Layout Budget-Friendly Options
 6. Navigating Emc And System Esd Design Guidelines For Board Layout eBook Formats
 - ePub, PDF, MOBI, and More
 - Emc And System Esd Design Guidelines For Board Layout Compatibility with Devices
 - Emc And System Esd Design Guidelines For Board Layout Enhanced eBook Features
 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Emc And System Esd Design Guidelines For Board Layout
 - Highlighting and Note-Taking Emc And System Esd Design Guidelines For Board Layout
 - Interactive Elements Emc And System Esd Design Guidelines For Board Layout
 8. Staying Engaged with Emc And System Esd Design Guidelines For Board Layout
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Emc And System Esd Design Guidelines For Board Layout
 9. Balancing eBooks and Physical Books Emc And System Esd Design Guidelines For Board Layout
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Emc And System Esd Design Guidelines For Board Layout
 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
 11. Cultivating a Reading Routine Emc And System Esd Design Guidelines For Board Layout
 - Setting Reading Goals Emc And System Esd Design Guidelines For Board Layout
 - Carving Out Dedicated Reading Time
 12. Sourcing Reliable Information of Emc And System Esd Design Guidelines For Board Layout

- Fact-Checking eBook Content of Emc And System Esd Design Guidelines For Board Layout
 - Distinguishing Credible Sources
13. Promoting Lifelong Learning
- Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
14. Embracing eBook Trends
- Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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