

Embedded Systems

ARM Programming and Optimization

Jason D. Bakos

```
#pragma omp parallel for
for (i=0;i<N/4;i+=4) {
    asm("pld %[nextx]\n\t"
        "mov r8, [code4_addr]\n\t"
        "vld1.32 {q0}, [%x_addr]#128\n\t"
        "vld1.32 {q1}, [%x]#64\n\t"
        "vld1.32 {q2}, [%x]#128\n\t"
        "vld1.32 {q3}, [%x]#128\n\t"
        "vld1.32 {q4}, [%x]#128\n\t"
        "vld1.32 {q5}, [%x]#128\n\t"
        "vld1.32 {q6}, [%x]#128\n\t"
        "vld1.32 {q7}, [%x]#128\n\t"
        "vld1.32 {q8}, [%x]#128\n\t"
        "vmla.f32 q2, q0, q1\n\t"
        "vmla.f32 q3, q0, q2\n\t"
        "vmla.f32 q4, q0, q3\n\t"
        "vmla.f32 q5, q0, q4\n\t"
        "vmla.f32 q6, q0, q5\n\t"
        "vmla.f32 q7, q0, q6\n\t"
        "vmla.f32 q8, q0, q7\n\t"
        "vst1.32 {q0}, [%d_addr]#128\n\t"
        "vst1.32 {q1}, [%d_addr]#64\n\t"
        "vst1.32 {q2}, [%d_addr]#128\n\t"
        "vst1.32 {q3}, [%d_addr]#128\n\t"
        "vst1.32 {q4}, [%d_addr]#128\n\t"
        "vst1.32 {q5}, [%d_addr]#128\n\t"
        "vst1.32 {q6}, [%d_addr]#128\n\t"
        "vst1.32 {q7}, [%d_addr]#128\n\t"
        "vst1.32 {q8}, [%d_addr]#128\n\t"
        "drj#128\n\t" : : [x_addr]"r"(x[i]), [nextx]"r"(x[i+16]), [d_addr]"r"(d[i]), [code4_addr]"r"(code4_vector) : "r0", "r1", "r2", "r3", "q1", "q2", "q3");
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