

ABSTRACT

Magnetic RAM (MRAM) is a new memory technology with access and cost characteristics comparable to those of conventional dynamic RAM (DRAM) and the non-volatility of magnetic media such as disk. That is MRAM retains its memory even after removing power from the device. Such a non-volatile memory has important military applications for missiles and satellites. Clearly such a device could also have important commercial applications if the non-volatility were accomplished without impacting other properties of the memory, notably density, read and write speed, and lifetime. IBM in cooperation with Infineon is promising to launch this new technology ,that will eliminate the boot-up process of a computer and thus enable it to turn on as instantly as a television or radio, using memory cells based on magnetic tunnel junctions.

This paper discusses the following aspects in detail: Attractions of this new technology
How MRAM works MRAM Architecture
Magnetic Tunnel Junctions - future of MRAM Challenges faced Anticipated Applications

INTRODUCTION

You hit the power button on your television and it instantly comes to life. But do the same thing with your computer and you have to wait a few minutes while it goes through its boot up sequence. Why can't we have a computer that turns on as instantly as a television or radio? IBM, in cooperation with Infineon, is promising to launch a new technology in the next few years that will eliminate the boot-up process. Magnetic random access memory (MRAM) has the potential to store more data, access that data faster and use less power than current memory technologies. The key to MRAM is that, as its name suggests, it uses magnetism rather than electrical power to store data. This is a major leap from dynamic RAM (DRAM), the most common type of memory in use today, which requires a continuous supply of electricity and is terribly inefficient. Twenty-five years ago, DRAM overtook ferrite core memory in the race to rule the PC memory market. Now it looks like ferromagnetic technology could be making a comeback, with IBM Corp. and Infineon Technologies charging a joint team of 80 engineers and scientists with the task of making magnetic RAM (MRAM) a commercial reality within four years

2. ATTRACTIONS OF THIS NEW TECHNOLOGY

Consider what happens when power goes off while you are typing on your computer? Unless you are connected to an uninterruptible power supply you lose everything you were working on since you last saved the document. That's because your computer's random access memory (RAM), which stores information for fast access, can't function without power. The same goes for your cellphone and PDA. Both require a battery to keep the RAM intact with your phone numbers and personal data. But IBM researchers have developed a new form of RAM — magnetic RAM (MRAM) — that doesn't forget anything when the power goes out.

MRAM promises to be

Cheap

Fast

Nonvolatile

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Tareq Ahram



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Advances in Artificial Intelligence, Software and Systems Engineering Tareq Ahram,2019-06-10 This book addresses emerging issues resulting from the integration of artificial intelligence systems in our daily lives It focuses on the cognitive visual social and analytical aspects of computing and intelligent technologies highlighting ways to improve the acceptance effectiveness and efficiency of said technologies Topics such as responsibility integration and training are discussed throughout The book also reports on the latest advances in systems engineering with a focus on societal challenges and next generation systems and applications for meeting them The book is based on two AHFE 2019 Affiliated Conferences on Artificial Intelligence and Social Computing and on Service Software and Systems Engineering which were jointly held on July 24 28 2019 in Washington DC USA

Digital Transformation in Semiconductor Manufacturing Sophia Keil,Rainer Lasch,Fabian Lindner,Jacob Lohmer,2020-05-26 This open access book reports on cutting edge electrical engineering and microelectronics solutions to foster and support digitalization in the semiconductor industry Based on the outcomes of the European project iDev40 which were presented at the two first conference editions of the European Advances in Digital Transformation Conference EADCT 2018 and EADTC 2019 the book covers different multidisciplinary aspects related to digital transformation including technological and industrial developments as well as human factors research and applications Topics include modeling and simulation methods in semiconductor operations supply chain management issues employee training methods and workplaces optimization as well as smart software and hardware solutions for semiconductor manufacturing By highlighting industrially relevant developments and discussing open issues related to digital transformation the book offers a timely practice oriented guide to graduate students researchers and professionals interested in the digital transformation of manufacturing domains and work environments

Computer Safety, Reliability, and Security Alexander Romanovsky,Elena Troubitsyna,Illir Gashi,Erwin Schoitsch,Friedemann Bitsch,2019-09-02 This book constitutes the proceedings of the Workshops held in conjunction with SAFECOMP 2019 38th International Conference on Computer Safety Reliability and Security in September 2019 in Turku Finland The 32 regular papers included in this volume were carefully reviewed and selected from 43 submissions the book also contains two invited papers The workshops included in this volume are ASSURE 2019 7th International Workshop on Assurance Cases for Software Intensive Systems DECSoS 2019 14th ERCIM EWICS ARTEMIS Workshop on Dependable Smart Embedded and Cyber Physical Systems and Systems of Systems SASSUR 2019 8th International Workshop on Next Generation of System Assurance Approaches for Safety Critical Systems STRIVE 2019 Second International Workshop on Safety securiTy and pRivacy In automotiVe systEms WAISE 2019 Second International Workshop on Artificial Intelligence Safety Engineering

Online Engineering and Society 4.0 Michael E. Auer,Kalyan Ram Bhimavaram,Xiao-Guang Yue,2021-09-19 This book presents the general objective of the REV2021 conference which is to contribute and discuss fundamentals applications and experiences

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Computer Security - ESORICS 2003 Einar Snekkenes, 2003-09-29 This book constitutes the refereed proceedings of the 8th European Symposium on Research in Computer Security ESORICS 2003 held in Gjøvik Norway in October 2003 The 19 revised full papers presented were carefully reviewed and selected from 114 submissions Among the topics addressed are signature control access control key exchange broadcast protocols privacy preserving technologies attack analysis electronic voting identity control authentication security services smart card security formal security protocols analysis and intrusion detection

Photonische Netze Christian Schäffer, 2004 Proceedings of 3rd Electronics Packaging Technology Conference (EPTC 2000) Thiam Beng Lim, 2000 Topics covered by this title include packaging materials packaging trends thermal design and modelling solder joint metallurgy process and reliability modelling thermal characterization materials characterization techniques and assembly manufacturing technologies

Meeting Abstracts Electrochemical Society, 2002

Data Analysis and Modeling for Process Control, 2005 **Ultra Clean Processing of Silicon Surfaces V** Marc Heyns, Marc Meuris, Paul Mertens, 2001-01-02 UCPSS 2000 Optical Microlithography, 2001 *Integrated Circuit and System Design*, 2003

Proceedings of the ... IEEE Intelligent Vehicles Symposium, 2003 *Laser Diodes, Optoelectronic Devices, and Heterogenous Integration* Alfred Driessen, 2003 Conference Proceedings, 2000 2001 *International Symposium on VLSI Technology, Systems, and Applications* IEEE, Taipei Section Staff, 2001 *Annual Symposium on Photomask Technology*, 2002

European Conference on Mask Technology for Integrated Circuits and Microcomponents, 2004 *In-line Characterization, Yield Reliability, and Failure Analysis in Microelectronics Manufacturing*, 2001 *IEEE International Workshop on Rapid Systems Prototyping* IEEE Computer Society. Technical Committee on Simulation, 1999 Contains papers from a June 1999 workshop which brought together system designers model

and tool developers integrated circuit designers and software engineers to explore problems and techniques in the area of rapid system prototyping Papers focus on models for system simulation emulation in a hierarchical sense software to hardware mapping software prototyping and validation prototyping environments of hardware simulators and experiences from specific system prototyping projects Contains sections on communication and distributed systems reconfigurable architectures reuse formal methods design methodologies interface technologies and FPGA based design Lacks a subject index Annotation copyrighted by Book News Inc Portland OR

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Table of Contents Abstract 1 Introduction Infineon

1. Understanding the eBook Abstract 1 Introduction Infineon
 - The Rise of Digital Reading Abstract 1 Introduction Infineon
 - Advantages of eBooks Over Traditional Books
2. Identifying Abstract 1 Introduction Infineon
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Abstract 1 Introduction Infineon
 - User-Friendly Interface
4. Exploring eBook Recommendations from Abstract 1 Introduction Infineon
 - Personalized Recommendations
 - Abstract 1 Introduction Infineon User Reviews and Ratings
 - Abstract 1 Introduction Infineon and Bestseller Lists
5. Accessing Abstract 1 Introduction Infineon Free and Paid eBooks
 - Abstract 1 Introduction Infineon Public Domain eBooks
 - Abstract 1 Introduction Infineon eBook Subscription Services
 - Abstract 1 Introduction Infineon Budget-Friendly Options
6. Navigating Abstract 1 Introduction Infineon eBook Formats

- ePub, PDF, MOBI, and More
 - Abstract 1 Introduction Infineon Compatibility with Devices
 - Abstract 1 Introduction Infineon Enhanced eBook Features
7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Abstract 1 Introduction Infineon
 - Highlighting and Note-Taking Abstract 1 Introduction Infineon
 - Interactive Elements Abstract 1 Introduction Infineon
 8. Staying Engaged with Abstract 1 Introduction Infineon
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Abstract 1 Introduction Infineon
 9. Balancing eBooks and Physical Books Abstract 1 Introduction Infineon
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Abstract 1 Introduction Infineon
 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
 11. Cultivating a Reading Routine Abstract 1 Introduction Infineon
 - Setting Reading Goals Abstract 1 Introduction Infineon
 - Carving Out Dedicated Reading Time
 12. Sourcing Reliable Information of Abstract 1 Introduction Infineon
 - Fact-Checking eBook Content of Abstract 1 Introduction Infineon
 - Distinguishing Credible Sources
 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
 14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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