

AVR308: Software LIN Slave

Features

- Low-cost, No External Components
- Compatible with LIN Protocol Specification Version 1.0
- Size-efficient Code

Introduction

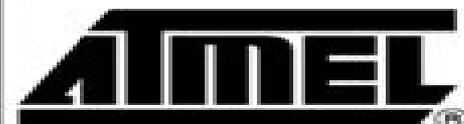
This application note shows how to implement a LIN (Local Interconnect Network) slave task in an 8-bit RISC AVR microcontroller without the need for any external components.

The LIN protocol is a serial communications protocol which efficiently supports control of mechatronic nodes in a distributed network. This makes the protocol ideal for use in automotive applications. A LIN network consist of a single Master and a set of Slave nodes. This application note show how to implement the protocol for the physical layer and the data link layer according to the two lowest levels of the ISO/OSI reference model. This provides the foundation for message transmissions between nodes in the network. The physical layer of the ISO/OSI model only provides a raw bit-stream between two nodes in the network. The data link layer makes the physical layer reliable by adding error detection and control. It also adds the means to activate, maintain, and deactivate the link. The higher levels of the ISO/OSI reference model is beyond the scope of the LIN protocol and thus not explained here. For now, this is up to the user to specify and implement.

The LIN protocol differs from the CAN protocol in the sense that it is below the performance and scope of the CAN. Its strongpoint is that it is provides a simple, low-cost solution for applications not requiring the power of the CAN protocol. The main properties of the LIN bus include:

- Single Master, multiple Slaves.
- Low-cost silicon implementation.
- Self synchronization without quartz or ceramic resonators in the Slave nodes.
- Guarantee of latency time for signal transmission.
- Single-wire implementation.
- Speeds up to 20 Kbits/s.

Figure 1. LIN Network Topology



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Microcontroller

Application
Note

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synthesis or processing software will welcome Arduino for Musicians *8051 Microcontrollers* Salvador Pinillos Gimenez,2018-05-22 This textbook describes in detail the fundamental information about the 8051 microcontroller and it carefully teaches readers how to use the microcontroller to make both electronics hardware and software In addition to discussion of the 8051 internals this text includes numerous solved examples end of chapter exercises laboratory and practical projects *1995 Product Line Databooks: Embedded microcontrollers* Intel Corporation,1993 **Embedded Microcontrollers** ,1995 **Computer Science and Engineering** Zainalabedin Navabi,David R. Kaeli,2009-08-10 Computer Science and Engineering is a component of Encyclopedia of Technology Information and Systems Management Resources in the global Encyclopedia of Life Support Systems EOLSS which is an integrated compendium of twenty one Encyclopedias The Theme on Computer Science and Engineering provides the essential aspects and fundamentals of Hardware Architectures Software Architectures Algorithms and Data Structures Programming Languages and Computer Security It is aimed at the following five major target audiences University and College students Educators Professional practitioners Research personnel and Policy analysts managers and decision makers **AVR RISC Microcontroller Handbook** Claus Kuhnel,1998 The AVR RISC Microcontroller Handbook is a comprehensive guide to designing with Atmel s new controller family which is designed to offer high speed and low power consumption at a lower cost The main text is divided into three sections hardware which covers all internal peripherals software which covers programming and the instruction set and tools which explains using Atmel s Assembler and Simulator available on the Web as well as IAR s C compiler Practical guide for advanced hobbyists or design professionals Development tools and code available on the Web *Application Notes for 80C51-based 8-bit Microcontrollers* Philips,1993 **IC Master** ,2001 *Application Notes for 80C51-based 8-bit Microcontrollers* ,1993 **Digital Logic** M. Rafiquzzaman,Steven A. McNinch,2019-08-21 Digital Logic with an Introduction to Verilog and FPGA Based Design provides basic knowledge of field programmable gate array FPGA design and implementation using Verilog a hardware description language HDL commonly used in the design and verification of digital circuits Emphasizing fundamental principles this student friendly textbook is an ideal resource for introductory digital logic courses Chapters offer clear explanations of key concepts and step by step procedures that illustrate the real world application of FPGA based design Designed for beginning students familiar with DC circuits and the C programming language the text begins by describing of basic terminologies and essential concepts of digital integrated circuits using transistors Subsequent chapters cover device level and logic level design in detail including combinational and sequential circuits used in the design of microcontrollers and microprocessors Topics include Boolean algebra and functions analysis and design of sequential circuits using logic gates FPGA based implementation using CAD software tools and combinational logic design using various HDLs with focus on Verilog *The Motorola MC68332 Microcontroller* Thomas L. Harman,1991 M CREATED *Peripheral Components* ,1993 *EDN, Electrical Design News* ,2007 **Linear Applications Handbook**

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