

Digital Logic Design - Asynchronous Counter

③ 状态转换表



① 输入方程

$$J_1 = K_1 = 1 \quad CP_1 \downarrow$$

$$J_2 = K_2 = 1 \quad CP_2 = Y_1 \downarrow$$

$$J_3 = K_3 = 1 \quad CP_3 = Y_2 \downarrow$$

异步Moor
型时序电路

异步模8加
法计数器

现态			次态			时钟		
Y_3^n	Y_2^n	Y_1^n	Y_3^{n+1}	Y_2^{n+1}	Y_1^{n+1}	CP_3	CP_2	CP_1
0	0	0	0	0	1	无	无	↓
0	0	1	0	1	0	无	↓	↓
0	1	0	0	1	1	无	无	↓
0	1	1	1	0	0	↓	↓	↓
1	0	0	1	0	1	无	无	↓
1	0	1	1	1	0	无	↓	↓
1	1	0	1	1	1	无	无	↓
1	1	1	0	0	0	↓	↓	↓

② 次态方程

$$Y_1^{n+1} = J_1 \bar{Q}_1 + \bar{K}_1 Q_1 = \bar{Y}_1 \quad CP_1 \downarrow$$

$$Y_2^{n+1} = J_2 \bar{Q}_2 + \bar{K}_2 Q_2 = \bar{Y}_2 \quad Y_1 \downarrow$$

$$Y_3^{n+1} = J_3 \bar{Q}_3 + \bar{K}_3 Q_3 = \bar{Y}_3 \quad Y_2 \downarrow$$

④ 波形图

二分频



Digital Logic Design Problems And Solutions

J Rink



Digital Logic Design Problems And Solutions:

Decoding **Digital Logic Design Problems And Solutions**: Revealing the Captivating Potential of Verbal Expression

In a period characterized by interconnectedness and an insatiable thirst for knowledge, the captivating potential of verbal expression has emerged as a formidable force. Its capability to evoke sentiments, stimulate introspection, and incite profound transformations is genuinely awe-inspiring. Within the pages of "**Digital Logic Design Problems And Solutions**," a mesmerizing literary creation penned by way of a celebrated wordsmith, readers attempt an enlightening odyssey, unraveling the intricate significance of language and its enduring affect our lives. In this appraisal, we shall explore the book is central themes, evaluate its distinctive writing style, and gauge its pervasive influence on the hearts and minds of its readership.

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