

Learning Switched Mode Power Supply Design Using MATLAB/SIMULINK

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Abstract—Switched Mode Power Supplies (SMPS) have become an indispensable part of modern electronic systems. In this paper, a generalized MATLAB/SIMULINK modeling technique is presented. The proposed techniques have been successfully integrated into SMPS design courses at Lunghwa University, Taiwan. The main objective of the course is to teach final year undergraduate students and first year Master's students how to design an SMP in real life. Positive response from students indicates that the proposed technique is useful for SMPS design courses.

Keywords—switched mode power supply (SMPS), MATLAB/SIMULINK, pulsewidth modulation (PWM), Power factor correction (PFC)

I. INTRODUCTION

As to today more than 75% of all generated power was processed by power electronics. The extensive usage of switching converter circuits in electronic products and systems makes the fundamental understanding of power electronics a necessity for students and electronic engineers. This trend has resulted in an increasing interest in providing power electronic courses at the senior undergraduate and the graduate levels in various institutions [1-3]. Traditionally, SMPS are taught as part of a typical introductory undergraduate elective course in power electronics. However, the practical design of SMPS calls for design expertise which can only be gained with years of experience. Fig. 1 shows the block diagram of a typical SMPS, the AC input voltage is rectified by a full-wave bridge rectifier. The rectified input voltage is next applied to power factor correction (PFC) pre-regulator followed by output DC-DC converter(s). Downstream DC-DC converter generates a set of DC voltages required for the load, and normally also provides input-to-output isolation. There are a number of topologies utilized in a DC-DC converter, and a large variety of regulator ICs suitable for each of these topologies. The selection of right topology and controller depends on specific requirements for the power supply and personal experience of the designer.

In this paper, a generalized MATLAB/SIMULINK modeling techniques for SMPS is presented. The objective of the proposed technique is to assist the learning of SMPS through simulation. A practical SMPS design consists of several parts: power converter topology selection, controller (compensation circuit) design, magnetic design and

Electromagnetic Compatibility (EMC). Among them, the magnetic and EMC design depend on the real-world design, therefore is often not taken into account in simulation level. In this paper, a description of the modeling techniques for power converter and control ICs along with their use in SMPS courses will be presented. The various types of converters are considered to be examples to clearly show the validity of the proposed technique, and will be described in detail in the following sections.



Fig. 1 Typical SMPS block diagram

II. SIMULINK MODEL OF SWITCHED MODE POWER SUPPLY

In this section, the generalized construction method of SMPS SIMULINK model will be presented. In this paper, the behavior model technique as presented in [4] is adopted. Take the flyback power converter shown in Fig. 2 as an example. In a flyback converter, the switch Q is continuously switching on and off with high frequency in order to provide the transfer of electric power via energy storage components. Assuming the flyback converter operates in continuous conduction mode (CCM), the state equation of the power circuit when Q is on can be written as

$$\frac{dv_1}{dt} = \frac{1}{L} (v_1 - i_1 R_1) \quad (1)$$

$$\frac{dv_2}{dt} = -\frac{1}{C} \frac{v_2}{R_2} \quad (2)$$

and the relationship between the transformer voltage on the primary side v_1 and secondary side v_2 can be shown as

$$v_1 = v_2 \quad (3)$$

$$v_2 = -\frac{n_2}{n_1} v_1 = -\frac{n_2}{n_1} v_1 \quad (4)$$

where n_1 is the number of turns of primary side, and n_2 is the number of turns of secondary side.

When the switch Q turned off, the state equation of the power circuit can be written as

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Table of Contents Design Of Switched Mode Power Supply Using Matlab Simulink

1. Understanding the eBook Design Of Switched Mode Power Supply Using Matlab Simulink
 - The Rise of Digital Reading Design Of Switched Mode Power Supply Using Matlab Simulink
 - Advantages of eBooks Over Traditional Books
2. Identifying Design Of Switched Mode Power Supply Using Matlab Simulink
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Design Of Switched Mode Power Supply Using Matlab Simulink
 - User-Friendly Interface
4. Exploring eBook Recommendations from Design Of Switched Mode Power Supply Using Matlab Simulink

- Personalized Recommendations
 - Design Of Switched Mode Power Supply Using Matlab Simulink User Reviews and Ratings
 - Design Of Switched Mode Power Supply Using Matlab Simulink and Bestseller Lists
5. Accessing Design Of Switched Mode Power Supply Using Matlab Simulink Free and Paid eBooks
 - Design Of Switched Mode Power Supply Using Matlab Simulink Public Domain eBooks
 - Design Of Switched Mode Power Supply Using Matlab Simulink eBook Subscription Services
 - Design Of Switched Mode Power Supply Using Matlab Simulink Budget-Friendly Options
 6. Navigating Design Of Switched Mode Power Supply Using Matlab Simulink eBook Formats
 - ePub, PDF, MOBI, and More
 - Design Of Switched Mode Power Supply Using Matlab Simulink Compatibility with Devices
 - Design Of Switched Mode Power Supply Using Matlab Simulink Enhanced eBook Features
 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Design Of Switched Mode Power Supply Using Matlab Simulink
 - Highlighting and Note-Taking Design Of Switched Mode Power Supply Using Matlab Simulink
 - Interactive Elements Design Of Switched Mode Power Supply Using Matlab Simulink
 8. Staying Engaged with Design Of Switched Mode Power Supply Using Matlab Simulink
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Design Of Switched Mode Power Supply Using Matlab Simulink
 9. Balancing eBooks and Physical Books Design Of Switched Mode Power Supply Using Matlab Simulink
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Design Of Switched Mode Power Supply Using Matlab Simulink
 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
 11. Cultivating a Reading Routine Design Of Switched Mode Power Supply Using Matlab Simulink
 - Setting Reading Goals Design Of Switched Mode Power Supply Using Matlab Simulink
 - Carving Out Dedicated Reading Time
 12. Sourcing Reliable Information of Design Of Switched Mode Power Supply Using Matlab Simulink

- Fact-Checking eBook Content of Design Of Switched Mode Power Supply Using Matlab Simulink
 - Distinguishing Credible Sources
13. Promoting Lifelong Learning
- Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
14. Embracing eBook Trends
- Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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