

# Semiconductor Devices

## Short Notes

### PN Junction

**P-type:** When trivalent impurities are added or doped to pure (intrinsic) semiconductor, p-type semiconductor is formed.

Trivalent Impurities → Gallium, Indium, etc.

**N-type:** When pentavalent impurities are added or doped to pure semiconductor, n-type semiconductor is formed.

Pentavalent Impurities → Antimony, Arsenic

**P-type**  
Holes (+ve charge)  
Majority charge carriers  
Electrons (-ve charge)  
Minority charge carriers

**N-type**  
Electrons  
Majority charge carriers  
Holes  
Minority charge carriers

Do majority and minority charge carriers vary? → 100% P-type or 100% N-type semiconductor contains some amount of electron in holes and N-type contains some amount of holes in electrons. So, P-type has electron as minority charge carriers and N-type has holes as minority charge carriers.



If P-type & N-type are attached, diffusion phenomena occurs. Charges get diffused or moved to another end, which creates a potential barrier or depletion layer.



### PN Diode (Diode - Device)

Diode is a device made by use of semiconductor.



Fig: Diode



Fig: Symbolical representation of PN Junction diode

### Forward Biasing of Diode



Fig: Forward Biasing. Positive charges have to get holes, and negative charges have to get electrons. In the depletion layer, there is a flow of charge, electricity through the diode.



Fig: Reverse Biasing. No flow of electricity. Yes, there is a -ve charge attraction, but the side has accumulated positive charge, so electricity does not flow.

### Forward Biasing Characteristics



Fig: Circuit diagram of Forward Biasing



Fig: Forward characteristics

Series rheostat to help in voltage regulation, current control, etc. Yes, the meaning of potential barrier is potential difference. Voltage supply is not a potential barrier.

potential barrier. As voltage increases, gradual increase of current, at point where potential barrier is overcome, current sharply increases.

### Reverse Biasing Characteristics

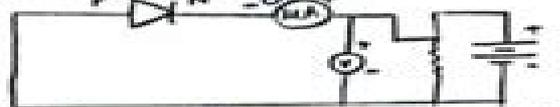


Fig: Circuit diagram of reverse biasing



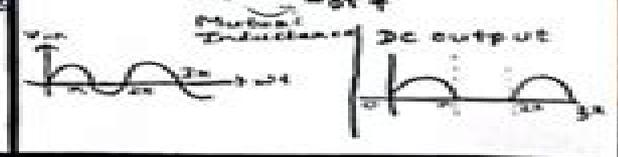
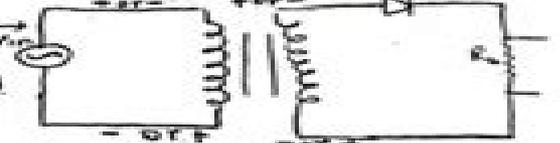
Fig: Reverse characteristics

Semiconductor is a pure state. In a pure state, there are electrons in P-type and holes in N-type. In a semiconductor, there are electrons as minority charge carriers and holes as majority charge carriers. Minority charge carriers move towards the positive terminal, and majority charge carriers move towards the negative terminal.

Yes, the breakdown voltage is a current sharply increase.

### Rectification: Process of converting AC to DC

#### 1. Half-wave Rectification



# Semiconductor 12th Class Chapter Notes

**Christian Drosten**



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