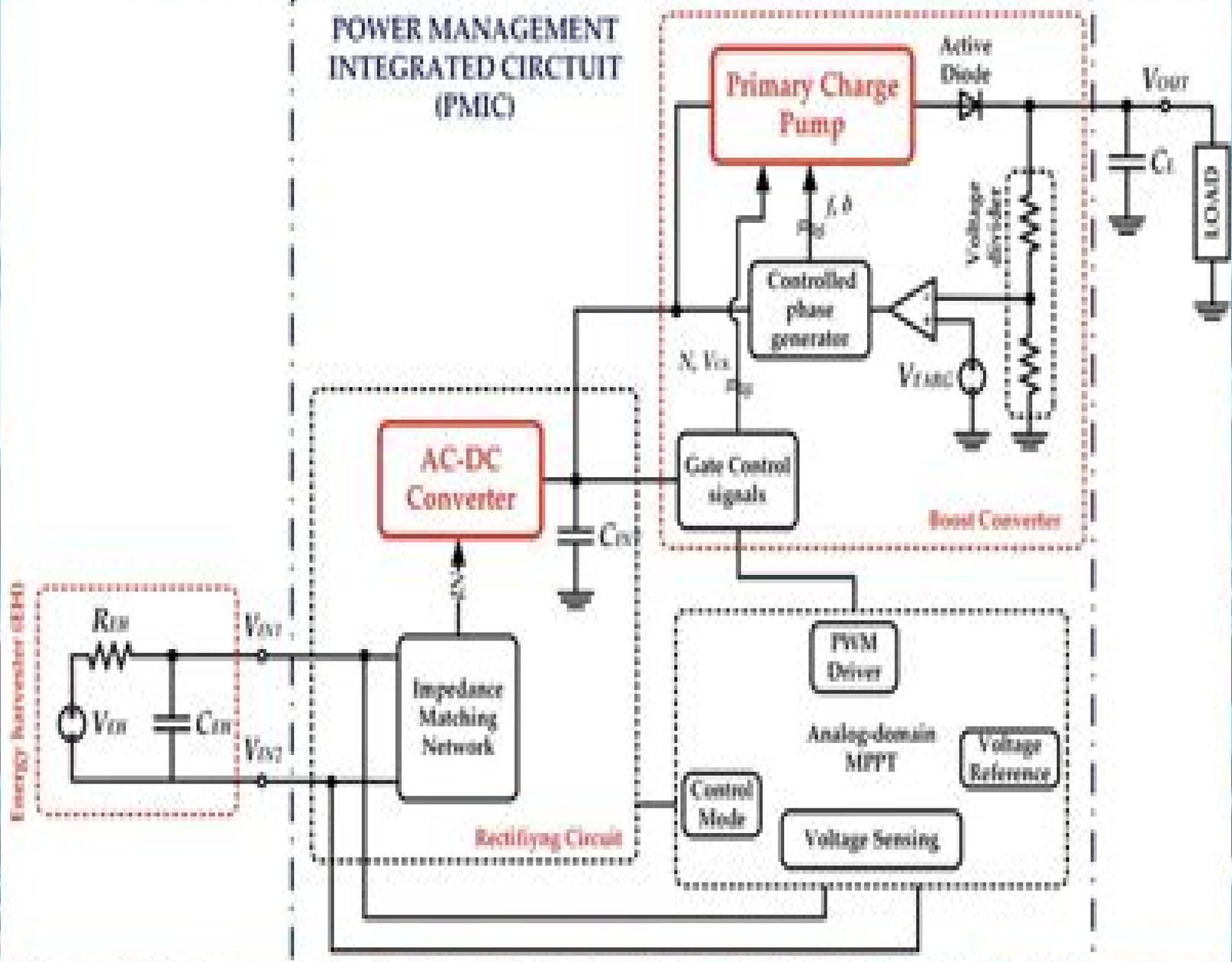


POWER MANAGEMENT INTEGRATED CIRCUIT (PMIC)



Power Management Integrated Circuit Analysis And Design

Judd E. Hollander



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Design of Power Management Integrated Circuits Bernhard Wicht, 2024-05-13 Design of Power Management Integrated Circuits Comprehensive resource on power management ICs affording new levels of functionality and applications with cost reduction in various fields Design of Power Management Integrated Circuits is a comprehensive reference for power management IC design covering the circuit design of main power management circuits like linear and switched mode voltage regulators along with sub circuits such as power switches gate drivers and their supply level shifters the error amplifier current sensing and control loop design Circuits for protection and diagnostics as well as aspects of the physical design like lateral and vertical power delivery pin out floor planning grounding supply guidelines and packaging are also addressed A full chapter is dedicated to the design of integrated passives The text illustrates the application of power management integrated circuits PMIC to growth areas like computing the Internet of Things mobility and renewable energy Includes numerous real world examples case studies and exercises illustrating key design concepts and techniques Offering a unique insight into this rapidly evolving technology through the author s experience developing PMICs in both the industrial and academic environment Design of Power Management Integrated Circuits includes information on Capacitive inductive and hybrid DC DC converters and their essential circuit blocks covering error amplifiers comparators and ramp generators Sensing protection and diagnostics covering thermal protection inductive loads and clamping structures under voltage reference and power on reset generation Integrated MOS MOM and MIM capacitors integrated inductors Control loop design and PWM generation ensuring stability and fast transient response subharmonic oscillations in current mode control analysis and circuit design for slope compensation DC behavior and DC related circuit design covering power efficiency line and load regulation error amplifier dropout and power transistor sizing Commonly used level shifters including sizing rules and

cascaded tapered driver sizing and optimization guidelines Optimizing the physical design considering packaging floor planning EMI pinout PCB design and thermal design Design of Power Management Integrated Circuits is an essential resource on the subject for circuit designers IC designers system engineers and application engineers along with advanced undergraduate students and graduate students in related programs of study

Power Management Integrated Circuits Amit Patra, Shailendra Baranwal, Ashis Maity, Samiran Dam, Syed Asif Eqbal, 2024-09-09 This book intends to be a comprehensive text on the topic of integrated circuits for power management putting together both theoretical foundations and practical details leading to successful design practices in research and industry It covers all the three main categories of power management circuits viz linear regulators inductor based switchers and switched capacitor circuits and presents detailed discussion of their common topologies operation and modeling Features Includes underlying theory and design implementation practical ingredients for power management integrated circuits PMICs Provides in depth analysis of topologies and circuits related to linear regulators switched capacitor converters and inductor based converters Covers all the relevant topics at the intersection between power electronics and integrated circuit design areas Provides guidelines for design of circuits and solutions for all the pertinent topologies Indicates all important issues and the related trade offs in the design of PMICs The book will be a valuable resource for senior and graduate level students as well as industry professionals who have done university level courses on analog circuit design control systems and power electronics

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