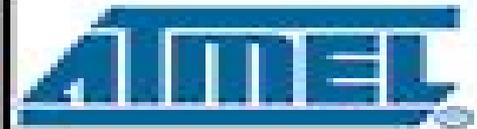


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- High-performance, Low-power AVR[®] 8-bit Microcontroller
- Advanced RISC Architecture
 - 130 Powerful Instructions – Most Single-Cycle Execution
 - 32 x 8 General-Purpose Working Registers – Peripheral Control Registers
 - Fully Static Operation
 - Up to 16 MHz Throughput at 1.8V
 - On-chip 2-cycle Multiplier
- High Endurance Non-volatile Memory segments
 - 128K Bytes of In-System Self-programmable Flash program memory
 - 8K Bytes EEPROM
 - 8K Bytes Internal SRAM
 - Write/Erase Cycle: 10,000 Flash/100,000 EEPROM
 - Data Retention: 20 years at 85°C/100 years at 25°C
 - Optional Boot Code Section with Independent Lock Bits
 - In-System Programming by On-chip Boot Program
 - True Read-While-Write Operation
 - Up to 64K Bytes Optional External Memory Space
 - Programming Lock for Software Security
 - SPI Interface for In-System Programming
- JTAG/JTAG and ICD.2 Compliant Interface
 - Background Capabilities According to the JTAG Standard
 - Extensive On-chip Debug Support
 - Programming of Flash, EEPROM, Fuses and Lock Bits through the JTAG Interface
 - 20 Pins
 - 10 Pins with Separate Prescaler and Compare Modes
 - 10 Pins with Separate Prescaler, Compare Mode and
 - 10 Pins
 - Real-Time Counter with Prescaler and Compare Modes
 - Two 8-bit Timers with Prescaler and Compare Modes
 - 8 Pulse-Width Modulators (PWM) with Prescaler and Compare Modes
 - Output Compare Module
 - Built-in, 10-bit ADC
 - Single-ended Channels
 - 2 Differential Channels
 - 2 Differential Channels with Programmable Gain of 1x, 10x, or 20x
 - Byte-oriented Two-wire Serial Interface
 - Dual Programmable Serial USARTs
 - MicroSlave[™] SPI Serial Interface
 - Programmable Watchdog Timer with On-chip Oscillator
 - On-chip Analog Comparator
- Special Microcontroller Features
 - Power-on Reset and Programmer Brownout Detection
 - Internal Calibrated RC Oscillator
 - External and Internal Interrupt Sources
 - Six Sleep Modes: Idle, ADC Noise Reduction, Power-save, Power-down, Standby, and Extended Standby
 - Software Selectable Clock Frequency
 - ATmega100 Compatibility Mode Selected by a Fuse
 - Global Pull-up Enable
- I/O and Packages
 - 33 Programmable I/O Lines
 - 8Kbit TQFP and 48-pin QFN
- Operating Voltages
 - 1.8 – 5.5V
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8-bit AVR[®] Microcontroller with 128K Bytes In-System Programmable Flash

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Table of Contents Atmel Avr Atmega128a Datasheet Atmel Corporation

1. Understanding the eBook Atmel Avr Atmega128a Datasheet Atmel Corporation
 - The Rise of Digital Reading Atmel Avr Atmega128a Datasheet Atmel Corporation
 - Advantages of eBooks Over Traditional Books
2. Identifying Atmel Avr Atmega128a Datasheet Atmel Corporation
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Atmel Avr Atmega128a Datasheet Atmel Corporation
 - User-Friendly Interface
4. Exploring eBook Recommendations from Atmel Avr Atmega128a Datasheet Atmel Corporation
 - Personalized Recommendations
 - Atmel Avr Atmega128a Datasheet Atmel Corporation User Reviews and Ratings
 - Atmel Avr Atmega128a Datasheet Atmel Corporation and Bestseller Lists
5. Accessing Atmel Avr Atmega128a Datasheet Atmel Corporation Free and Paid eBooks

- Atmel Avr Atmega128a Datasheet Atmel Corporation Public Domain eBooks
 - Atmel Avr Atmega128a Datasheet Atmel Corporation eBook Subscription Services
 - Atmel Avr Atmega128a Datasheet Atmel Corporation Budget-Friendly Options
6. Navigating Atmel Avr Atmega128a Datasheet Atmel Corporation eBook Formats
 - ePub, PDF, MOBI, and More
 - Atmel Avr Atmega128a Datasheet Atmel Corporation Compatibility with Devices
 - Atmel Avr Atmega128a Datasheet Atmel Corporation Enhanced eBook Features
 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Atmel Avr Atmega128a Datasheet Atmel Corporation
 - Highlighting and Note-Taking Atmel Avr Atmega128a Datasheet Atmel Corporation
 - Interactive Elements Atmel Avr Atmega128a Datasheet Atmel Corporation
 8. Staying Engaged with Atmel Avr Atmega128a Datasheet Atmel Corporation
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Atmel Avr Atmega128a Datasheet Atmel Corporation
 9. Balancing eBooks and Physical Books Atmel Avr Atmega128a Datasheet Atmel Corporation
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Atmel Avr Atmega128a Datasheet Atmel Corporation
 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
 11. Cultivating a Reading Routine Atmel Avr Atmega128a Datasheet Atmel Corporation
 - Setting Reading Goals Atmel Avr Atmega128a Datasheet Atmel Corporation
 - Carving Out Dedicated Reading Time
 12. Sourcing Reliable Information of Atmel Avr Atmega128a Datasheet Atmel Corporation
 - Fact-Checking eBook Content of Atmel Avr Atmega128a Datasheet Atmel Corporation
 - Distinguishing Credible Sources
 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development

- Exploring Educational eBooks
14. Embracing eBook Trends
- Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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