



MOTOROLA

QUAD ANALOG SWITCH/QUAD MULTIPLEXER

The MC14066B consists of four independent switches capable of controlling either digital or analog signals. This quad bilateral switch is useful in signal gating, chopper, modulator, demodulator and CMOS logic implementation.

The MC14066B is designed to be pin-for-pin compatible with the MC14016B, but has much lower ON resistance. Input voltage swings as large as the full supply voltage can be controlled via each independent control input.

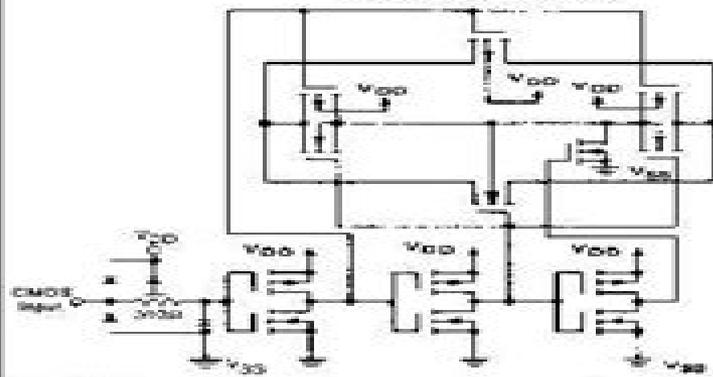
- Triple Diode Protection on All Control Inputs
- Supply Voltage Range = 3.0 Vdc to 18 Vdc
- Linearized Transfer Characteristics
- Low Noise — 10 nV/√Cycle; f_c = 1.0 kHz typical
- Pin-for-Pin Replacement for CD4016, CD4016, MC14016B
- For Lower R_{ON}, Use The HC4066 High-Speed CMOS Device

MAXIMUM RATINGS* (Voltage References to V_{DD})

Symbol	Parameter	Value	Unit
V _{DD}	DC Supply Voltage	0.5 to +18.0	V
V _{IN} , V _{OUT}	Input or Output Voltage (DC or Transient)	-0.5 to V _{DD} + 0.5	V
I _{IN}	Input Current (DC or Transient), per Control Pin	-1.0	mA
I _{OUT}	Switch Through Current	-25	mA
P _{tot}	Power Dissipation, per Package†	500	mW
T _{stg}	Storage Temperature	-65 to +150	°C
T _l	Lead Temperature (5-Second Soldering)	260	°C

*Maximum Ratings are those values below which damage to the device may occur. †Temperature Coefficient: Plastic "P" and DIP† Packages: -2.5 mW/°C From 25°C To 125°C; Ceramic "C" Packages: -1.0 mW/°C From 100°C To +25°C

CIRCUIT SCHEMATIC
(1/4 OF DEVICE SHOWN)



MC14066B



L SUFFIX
CERAMIC
CASE 032



P SUFFIX
PLASTIC
CASE 040

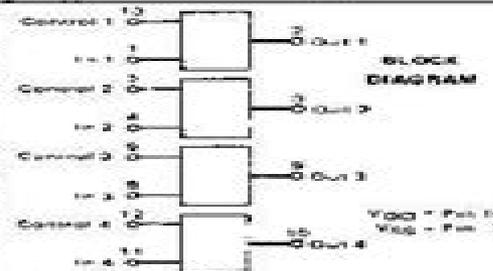


D SUFFIX
SOIC
CASE 75A

ORDERING INFORMATION

MC14066BCP Plastic
MC14066BP Ceramic
MC14066BD SOIC

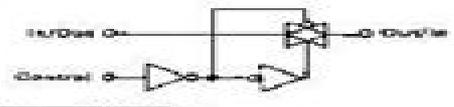
T_A = -50° to 125°C for all packages.



BLOCK DIAGRAM

V_{DD} = Pin 16
V_{SS} = Pin 7

LOGIC DIAGRAM AND TRUTH TABLE
(1/4 OF DEVICE SHOWN)



Control	Switch
0 = V _{DD}	OFF
1 = V _{SS}	ON

Logic Diagram Restrictions:
V_{DD} & V_{SS} < V_{DD}
V_{IN} < V_{OUT} < V_{DD}

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Jim Carlton



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