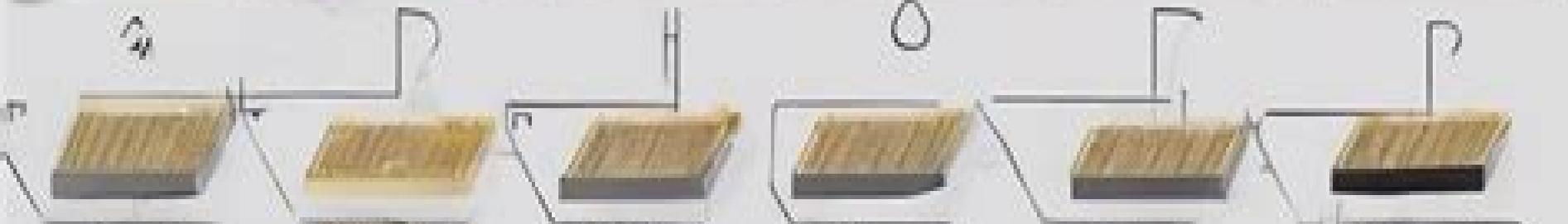
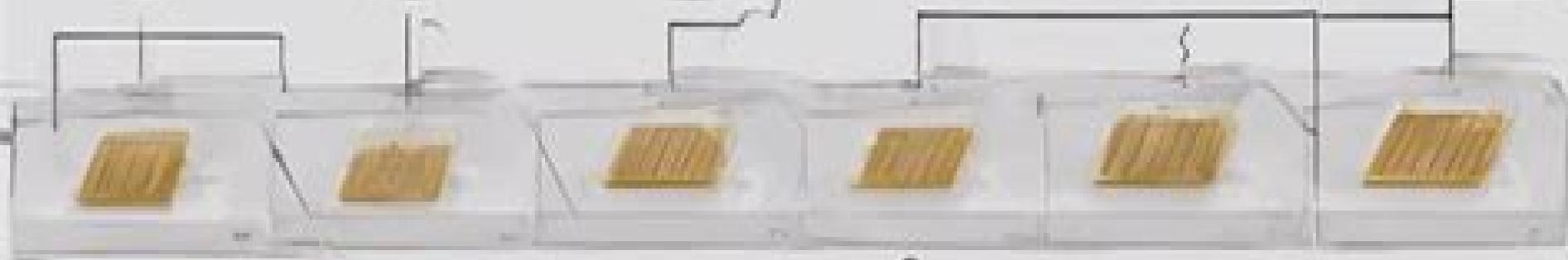


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Area Array Interconnection Handbook Karl J. Puttlitz, Paul A. Totta, 2012-12-06 Microelectronic packaging has been recognized as an important enabler for the solid state revolution in electronics which we have witnessed in the last third of the twentieth century Packaging has provided the necessary external wiring and interconnection capability for transistors and integrated circuits while they have gone through their own spectacular revolution from discrete device to gigascale integration At IBM we are proud to have created the initial simple concept of flip chip with solder bump connections at a time when a better way was needed to boost the reliability and improve the manufacturability of semiconductors The basic design which was chosen for SLT Solid Logic Technology in the 1960s was easily extended to integrated circuits in the 70s and VLSI in the 80s and 90s Three I O bumps have grown to 3000 with even more anticipated for the future The package families have evolved from thick film SLT to thin film metallized ceramic to co fired multi layer ceramic A later family or ceramics with matching expansivity to silicon and copper internal wiring was developed as a predecessor of the chip interconnection revolution in copper multilevel submicron wiring Powerful server packages have been developed in which the combined chip and package copper wiring exceeds a kilometer All of this was achieved with the constant objective of minimizing circuit delays through short efficient interconnects

The International Journal of Microcircuits and Electronic Packaging, 2001

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free interconnects Electromigration of lead free microbumps for 3D IC integration 3D IC Integration and Packaging John H. Lau,2015-07-06 A comprehensive guide to 3D IC integration and packaging technology 3D IC Integration and Packaging fully explains the latest microelectronics techniques for increasing chip density and maximizing performance while reducing power consumption Based on a course developed by its author this practical guide offers real world problem solving methods and teaches the trade offs inherent in making system level decisions Explore key enabling technologies such as TSV thin wafer strength measurement and handling micro solder bumping redistribution layers interposers wafer to wafer bonding chip to wafer bonding 3D IC and MEMS LED and complementary metal oxide semiconductor image sensors integration Assembly thermal management and reliability are covered in complete detail 3D IC Integration and Packaging covers 3D integration for semiconductor IC packaging Through silicon vias modeling and testing Stress sensors for thin wafer handling and strength measurement Package substrate technologies Microbump fabrication assembly and reliability 3D Si integration 2.5D 3D IC integration 3D IC integration with passive interposer Thermal management of 2.5D 3D IC integration Embedded 3D hybrid integration 3D LED and IC integration 3D MEMS and IC integration 3D CMOS image sensors and IC integration PoP chip to chip interconnects and embedded fan out WLP Printed Circuits Handbook, Seventh Edition Clyde F. Coombs,Happy Holden,2016-02-15 The world s leading guide to printed circuits completely updated to include the latest tools technology and techniques The de facto industry standard for over 30 years this practical guide equips you with definitive coverage of every facet of printed circuit assemblies from design methods to fabrication processes Now thoroughly revised and updated this book offers cutting edge coverage of printed circuit engineering fabrication construction soldering testing and repair Printed Circuits Handbook Seventh Edition features all new critical guidance on how to create manage and measure performance throughout the global supply chain Written by a team of international experts from both industry and academia this comprehensive volume offers new information on geographical specialization as well as the latest phase of the EUs Directive on the Restriction of Hazardous Substances ROHS II Fully overhauled to cover the latest scientific and technical developments Brand new coverage of printed circuit supply chain technology and geographical specialization Complete explanations of new EU safety directives for halogen free base materials **The Journal of the Acoustical Society of America** Acoustical Society of America,2000 **EDN** ,1999 Proceedings ,1997 **Low Cost Flip Chip Technologies** John H. Lau,2000 Of the Standard NuBGA Packages Thinner Substrate and Nonuniform Heat Spreader NuBGA Thermal Performance of the New NuBGA Package Temperature Distribution Thermal Resistance Cooling Power Wind Tunnel Experimental Analysis Solder Joint Reliability of the New NuBGA Package Electrical Performance of the New NuBGA Package Capacitance Inductance Summary of the New NuBGA Package Solder Bumped Flip Chip in PBGA Packages Intel s OLGA Package Technology OLGA Package Design OLGA Wafer Bumping OLGA Substrate Technology OLGA Package Assembly OLGA Package Reliability Mitsubishi s FC BGA Package Wafer Bumping Mitsubishi s SBU Substrate PC BGA

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