



Figure 2.0 Context Diagram of the Proposed Enrollment System

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web represent two unsigned 2 bit numbers the outputs are the four bits of the product of the input numbers express the logic functions for each term in the product on a karnaugh map of the four input variables hence design a multiplier circuit using 4 input nand and inverter gates only a 3 a 2 a 1 a 0 0001 1100

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