

Challenge Tutorial 01: Using Design Variables and Parametric Analysis

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Last Updated: 02/08/2016

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Introduction

Design variables provide a method for specifying critical parameters just before a simulation. This allows you to make quick changes to parameter values across single or multiple circuit components without having to edit any schematic components. Design variables can also be used in simple parametric analysis, which can change a design variable across a range of values and display multiple simulation results on a single plot.

Using Design Variables

This part of the tutorial will show you how to parametricize the width of the PCB and PTH on components of an circuit by using design variables. These variable values are then set using the **Running Through Environment** panel in simulation.

1. Make a copy of the circuit you have previously designed, or create a new one.
2. Open the **Parameters** of the circuit.
3. Edit each one of the PTHs parameter to output it, and name the parameter by using clicking on it and entering **Propagate**. For you can press "q" as shortcut.
4. Define the rest of the width from **pad** and **PTH**. This specifies that a variable called **W** will be used to set the width of the PTHs. When you do this, observe that values for "Characteristic Impedance Prop" change from a constant value to a formula that involves the variable **W**. Always check to make sure that formula is updated when you change the width to a variable otherwise, your simulation results will be incorrect. Click **OK** to apply the change.
5. Repeat this procedure to change the width of the PTHs to **W**.
6. Select **Change \rightarrow Check and Save**. Verify that there are no errors.
7. Launch the **Analysis Using Design Environment**.
8. Set up the simulation environment as you normally would for simulating a schematic. This includes specifying a simulation file and transient run length, making the SPICE model reader in the environment options, and selecting the outputs to be plotted on the schematic.
9. In **AN**, select **Variables \rightarrow Copy Hierarchical**. The design variables **W** and **g** should appear in the Design Variables area of the **Running Environment Window**.
10. In **AN**, select **Variables \rightarrow Edit**. In the **Value** box, type **1.0** and then apply the value by clicking **Apply**. Change the value of **g** to **0.0** and click **Apply**. Check the values have been assigned, click **OK**. See Figure 1 and 2 below.
11. In **AN**, use the connection by selecting **Simulation \rightarrow Variable and Run**. After loading the the simulation to complete, the results window will pop-up. The results should be

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