

# CBGS SCHEME

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15EC42



## Fourth Semester B.E. Degree Examination, July/August 2021 Microprocessor

Time: 3 Hrs.

Max. Marks: 80

Note: Answer any FIVE full questions.

1. a. With a relevant diagram, explain the register organization of 8086. (08 Marks)  
b. List any three the advantages of memory segmentation. (03 Marks)  
c. The contents of different registers are shown below. Form the effective addresses for the instructions given below. Offset (displacement) = 5000H.  
[AX] = 1000H, [BX] = 2000H, [SI] = 3000H, [DI] = 4000H, [BP] = 5000H, [SP] = 6000H, [CS] = 0000H, [DS] = 1000H, [SS] = 2000H, [IP] = 7000H.  
i) MOV AX, [5000H]  
ii) MOV AX, [BX]  
iii) MOV AX, 5000H[BX]  
iv) MOV AX, [BX][SI]  
v) MOV AX, 5000H [BX] [SI]. (05 Marks)
2. a. Explain PUSH AX and POP AX instructions with steps involved. (05 Marks)  
b. Write an 8086 ALP to multiply two 8 bit signed numbers. Give example for different cases. (06 Marks)  
c. If the machine code for OP CODE MOV is 100010, find the hex codes for,  
i) MOV AX, BX  
ii) MOV AX, [BX] (05 Marks)
3. a. Explain different string handling instructions. (05 Marks)  
b. Write an 8086 ALP for the addition of two  $3 \times 3$  matrices shown below. The matrices are stored in the form of lists (row wise). Store the result of addition in the third list.  
$$\text{MAT1} = \begin{bmatrix} 01\text{H} & 02\text{H} & 03\text{H} \\ 04\text{H} & 05\text{H} & 06\text{H} \\ 07\text{H} & 08\text{H} & 09\text{H} \end{bmatrix} \quad \text{MAT2} = \begin{bmatrix} 11\text{H} & 22\text{H} & 33\text{H} \\ 44\text{H} & 55\text{H} & 66\text{H} \\ 77\text{H} & 88\text{H} & 99\text{H} \end{bmatrix}$$
 (05 Marks)  
c. Explain the following instructions with example  
i) AAA ii) SAR iii) NEG. (06 Marks)
4. a. Explain the following assembler directives with example.  
i) ORG ii) PTR iii) PROC. (06 Marks)  
b. Write an 8086 ALP to convert an 8 bit binary number into equivalent ECD code. (05 Marks)  
c. If [CL] = 56, find the contents of register BL, after execution of following set of instructions  
MOV BL, 1  
MOV AL, 0  
UP : CMP CL, 0  
JZ END  
SUB CL, BL  
INC AL  
ADD BL, 02  
JMP UP  
END : MOV BL, AL (05 Marks)

Important Note : 1. On completing your answers, compulsorily draw diagonal cross lines on the remaining blank pages.  
2. Any revealing of identification, appeal to evaluator and/or equations written eg. 42+4=80, will be treated as malpractice.

# Microprocessor Question Paper Vtu

**AW Rasmussen**



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**Advanced Microprocessor & Microcontrollers** S. K. Venkata Ram, 2004      *MECHATRONICS & MICROPROCESSORS: AS PER REVISED VTU SYLLABUS* G.K. Vijayaraghavan K.P. Ramachandran, M.S. Balasundaram, 2009-05-01 Special Features This textbook is useful for the undergraduate students embarking introductory course in Mechatronics and Microprocessors and covers the revised syllabus prescribed by Visvesvaraya Technological University VTU Karnataka India with effect from 2008 for third year Mechanical Mechatronics and Automobile Engineering students 1 Updated coverage on microprocessors and programming as represented by the Syllabus Map 2 Working and applications provided for various components 3 Wide variety of solved problems with step by step solutions 4 Concepts well illustrated by labeled circuit diagrams 5 Related examples and microprocessors programs 6 Excellent pedagogy that includes 360 illustrations and line diagrams 60 solved examples 260 review questions 160 objective type questions 30 chapter end problems 50 explanatory examples Model question papers About The Book This textbook is useful for the undergraduate students embarking on an introductory course in Mechatronics and Microprocessors The text focuses and is written for engineering students and for those who would like to understand the principles of mechatronic systems and microprocessors However it is designed to meet with the requirements for mechanical manufacturing and automobile engineering programmes prescribed by the Visvesvaraya Technological University VTU Karnataka in India It covers the revised syllabus prescribed by VTU Karnataka with effect from 2008 for third year Mechanical Mechatronics and Automobile Engineering students Updated coverage on microprocessors and programming as represented by the Syllabus Map Working and applications provided for various components Wide variety of solved problems with step by step solutions Concepts well illustrated by labeled circuit diagrams Related examples and microprocessors programs Excellent pedagogy that includes 360 illustrations and line diagrams 60 solved examples 260 review questions 160 objective type questions 30 chapter end problems 50 explanatory examples Model question papers

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