

CBGS SCHEME

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15EC42



Fourth Semester B.E. Degree Examination, July/August 2021 Microprocessor

Time: 3 Hrs.

Max. Marks: 80

Note: Answer any FIVE full questions.

1. a. With a relevant diagram, explain the register organization of 8086. (08 Marks)
b. List any three the advantages of memory segmentation. (03 Marks)
c. The contents of different registers are shown below. Form the effective addresses for the instructions given below. Offset (displacement) = 5000H.
[AX] = 1000H, [BX] = 2000H, [SI] = 3000H, [DI] = 4000H, [BP] = 5000H, [SP] = 6000H, [CS] = 0000H, [DS] = 1000H, [SS] = 2000H, [IP] = 7000H.
i) MOV AX, [5000H]
ii) MOV AX, [BX]
iii) MOV AX, 5000H[BX]
iv) MOV AX, [BX] [SI]
v) MOV AX, 5000H [BX] [SI]. (05 Marks)
2. a. Explain PUSH AX and POP AX instructions with steps involved. (05 Marks)
b. Write an 8086 ALP to multiply two 8 bit signed numbers. Give example for different cases. (06 Marks)
c. If the machine code for OPCODE MOV is 100010, find the hex codes for,
i) MOV AX, BX
ii) MOV AX, [BX] (05 Marks)
3. a. Explain different string handling instructions. (05 Marks)
b. Write an 8086 ALP for the addition of two 3×3 matrices shown below. The matrices are stored in the form of lists (row wise). Store the result of addition in the third list.
$$\text{MAT1} = \begin{bmatrix} 01\text{H} & 02\text{H} & 03\text{H} \\ 04\text{H} & 05\text{H} & 06\text{H} \\ 07\text{H} & 08\text{H} & 09\text{H} \end{bmatrix} \quad \text{MAT2} = \begin{bmatrix} 11\text{H} & 22\text{H} & 33\text{H} \\ 44\text{H} & 55\text{H} & 66\text{H} \\ 77\text{H} & 88\text{H} & 99\text{H} \end{bmatrix}$$
 (05 Marks)
c. Explain the following instructions with example
i) AAA ii) SAR iii) NEG. (06 Marks)
4. a. Explain the following assembler directives with example.
i) ORG ii) PTR iii) PROC. (06 Marks)
b. Write an 8086 ALP to convert an 8 bit binary number into equivalent ECD code. (05 Marks)
c. If [CL] = 56, find the contents of register BL, after execution of following set of instructions
MOV BL, 1
MOV AL, 0
UP : CMP CL, 0
JZ END
SUB CL, BL
INC AL
ADD BL, 02
JMP UP
END : MOV BL, AL (05 Marks)

Important Note : 1. On completing your answers, compulsorily draw diagonal cross lines on the remaining blank pages.
2. Any revealing of identification, appeal to evaluator and/or equisation written eg. 42-V-30, will be treated as malpractice.

Microprocessor Question Paper Vtu

J Elliott



Microprocessor Question Paper Vtu:

The book delves into Microprocessor Question Paper Vtu. Microprocessor Question Paper Vtu is a vital topic that needs to be grasped by everyone, ranging from students and scholars to the general public. The book will furnish comprehensive and in-depth insights into Microprocessor Question Paper Vtu, encompassing both the fundamentals and more intricate discussions.

1. This book is structured into several chapters, namely:
 - Chapter 1: Introduction to Microprocessor Question Paper Vtu
 - Chapter 2: Essential Elements of Microprocessor Question Paper Vtu
 - Chapter 3: Microprocessor Question Paper Vtu in Everyday Life
 - Chapter 4: Microprocessor Question Paper Vtu in Specific Contexts
 - Chapter 5: Conclusion
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Table of Contents Microprocessor Question Paper Vtu

1. Understanding the eBook Microprocessor Question Paper Vtu
 - The Rise of Digital Reading Microprocessor Question Paper Vtu
 - Advantages of eBooks Over Traditional Books
2. Identifying Microprocessor Question Paper Vtu
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Microprocessor Question Paper Vtu
 - User-Friendly Interface
4. Exploring eBook Recommendations from Microprocessor Question Paper Vtu
 - Personalized Recommendations
 - Microprocessor Question Paper Vtu User Reviews and Ratings
 - Microprocessor Question Paper Vtu and Bestseller Lists
5. Accessing Microprocessor Question Paper Vtu Free and Paid eBooks
 - Microprocessor Question Paper Vtu Public Domain eBooks
 - Microprocessor Question Paper Vtu eBook Subscription Services
 - Microprocessor Question Paper Vtu Budget-Friendly Options
6. Navigating Microprocessor Question Paper Vtu eBook Formats
 - ePub, PDF, MOBI, and More
 - Microprocessor Question Paper Vtu Compatibility with Devices
 - Microprocessor Question Paper Vtu Enhanced eBook Features
7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Microprocessor Question Paper Vtu
 - Highlighting and Note-Taking Microprocessor Question Paper Vtu
 - Interactive Elements Microprocessor Question Paper Vtu
8. Staying Engaged with Microprocessor Question Paper Vtu

- Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Microprocessor Question Paper Vtu
9. Balancing eBooks and Physical Books Microprocessor Question Paper Vtu
- Benefits of a Digital Library
 - Creating a Diverse Reading Collection Microprocessor Question Paper Vtu
10. Overcoming Reading Challenges
- Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
11. Cultivating a Reading Routine Microprocessor Question Paper Vtu
- Setting Reading Goals Microprocessor Question Paper Vtu
 - Carving Out Dedicated Reading Time
12. Sourcing Reliable Information of Microprocessor Question Paper Vtu
- Fact-Checking eBook Content of Microprocessor Question Paper Vtu
 - Distinguishing Credible Sources
13. Promoting Lifelong Learning
- Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
14. Embracing eBook Trends
- Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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